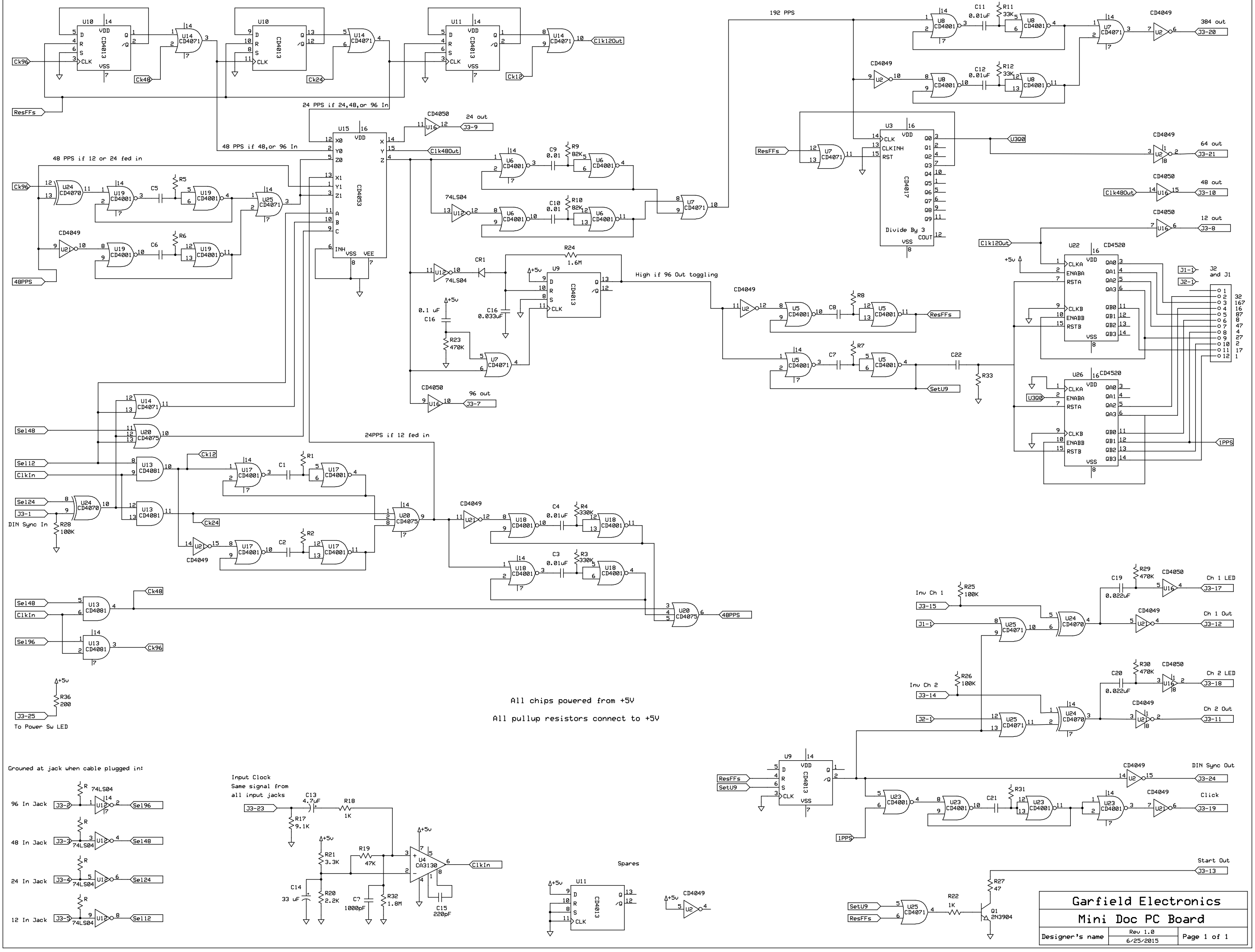
Ck96
Ck48
Ck24
Ck12
Clk12Out
ResFFs
24 PPS if 24,48,or 96 In
48 PPS if 48,or 96 In
48 PPS if 12 or 24 fed in
24 out
Clk48Out
74LS04
96 out
48PPS
192 PPS
High if 96 Out toggling
384 out
64 out
48 out
12 out
U300
SetU9
1PPS
Sel48
Sel12
ClkIn
Sel24
Sel96
24PPS if 12 fed in
DIN Sync In
J3-1
R28
100K
Ck12
Ck24
Ck48
Ck96
48PPS
All chips powered from +5V
All pullup resistors connect to +5V
Grounded at jack when cable plugged in:
96 In Jack
48 In Jack
24 In Jack
12 In Jack
Input Clock
Same signal from
all input jacks
J3-23
Spares
Inv Ch 1
Inv Ch 2
Ch 1 LED
Ch 1 Out
Ch 2 LED
Ch 2 Out
DIN Sync Out
Click
Start Out
J3-15
J3-14
J3-17
J3-12
J3-18
J3-11
J3-24
J3-19
J3-13
J1-1
J2-1
J2
and J1
R25
100K
R26
100K
R29
470K
R30
470K
C19
0.022uF
C20
0.022uF
R27
47
R22
1K
Q1
2N3904
To Power Sw LED
J3-25
R36
200
Garfield Electronics
Mini Doc PC Board
Designer's name
Rev 1.0
6/25/2015
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